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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/741,821	12/22/2000	Terry R. Lee	M4065.0406/P406	9458
24998	7590 06/18/2003			
DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP			EXAMINER	
2101 L STRE WASHINGT	ON, DC 20037-1526		DANG, KHANH NMN	
			ART UNIT	PAPER NUMBER
·			2181	6
		·	DATE MAILED: 06/18/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
1	·,	09/741,821	LEE ET AL.			
	Office Action Summary	Examiner	Art Unit			
omee near cummary			2181			
	The MAILING DATE of this communication ap	Khanh Dang pears on the cover sheet with the				
Period fo		F				
THE - Exte after - If the - If NC - Failu - Any I	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1. SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a repoperiod for reply is specified above, the maximum statutory period ure to reply within the set or extended period for reply will, by statut reply received by the Office later than three months after the mailined patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be to ly within the statutory minimum of thirty (30) da will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDON	imely filed ays will be considered timely. In the mailing date of this communication. IED (35 U.S.C. § 133).			
1)	Responsive to communication(s) filed on					
2a) □		his action is non-final.				
3)	, -					
Disposit	ion of Claims					
4) 🖂	Claim(s) 1-61 is/are pending in the application	n.				
	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) 🗌	Claim(s) is/are allowed.	٠				
6)⊠	Claim(s) <u>1-61</u> is/are rejected.					
7)	Claim(s) is/are objected to.					
-	Claim(s) are subject to restriction and/oion Papers	or election requirement.				
9) 🗌	The specification is objected to by the Examina	er.				
10) 🗌	The drawing(s) filed on is/are: a) acce	epted or b) objected to by the Ex	aminer.			
	Applicant may not request that any objection to the	ne drawing(s) be held in abeyance.	See 37 CFR 1.85(a).			
11) 🔲	The proposed drawing correction filed on	_ is: a)☐ approved b)☐ disappı	roved by the Examiner.			
If approved, corrected drawings are required in reply to this Office action.						
12) 🗌	The oath or declaration is objected to by the E	xaminer.				
Priority (under 35 U.S.C. §§ 119 and 120					
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) ☐ All b) ☐ Some * c) ☐ None of:						
	1. Certified copies of the priority documents have been received.					
	2. Certified copies of the priority documents have been received in Application No					
* 5	3. Copies of the certified copies of the price application from the International Bose the attached detailed Office action for a list	ureau (PCT Rule 17.2(a)).	-			
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).						
a) ☐ The translation of the foreign language provisional application has been received. 15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.						
Attachmen	t(s)					
2) Notic	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Information	ry (PTO-413) Paper No(s) I Patent Application (PTO-152)			
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DETAILED ACTION

Claim Rejections - 35 USC § 112

Claims 44 and 45 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claims 44 and 45, "A processor system" should be changed to – The data transmission system--.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

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The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

Claims 1-61 are rejected under 35 U.S.C. 102(b) as being anticipated by Holman et al. (WO 99/30240).

As broadly drafted these claims do not define any structure/step that differs from Holman et al. Similar claims will be grouped together to avoid repetition in explanation.

With regard to claims 1-61, Holman et al. discloses a memory module (396, for example), comprising: at least one memory device (312, for example); a data transfer interface (included in interface circuit 310, for example; see also Fig. 8 and description thereof) connected to a first data bus (323, for example) and to at least one memory device (312) by a second data bus (330, 332, 334-337, for example), the data transfer interface (310) comprising: a first bus segment (see at least Figs. 3 and 4) of the first data bus (323), the first data bus having a first number of data paths and said second data bus having a second number of data paths (as clearly shown in at least Figs. 3 and 4); a second bus segment (see at least Figs. 3 and 4 and description thereof) of said first data bus (323); an interface circuit (310, for example) connected between said first and second data buses, wherein said interface circuit is configured to selectively

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receive data on said first data bus and place said data on said second data bus, said interface circuit being connected to said first data bus between said first and second bus segments of said first data bus for passing data through from said first bus segment to said second bus segment and from said second bus segment to said first bus segment. The first data bus (323) is connected to a controller (304) and a processor (302). More specifically, with regard to claim 2 and similar claims, see page 13, second paragraph. With regard to claim 5 and similar claims, see page 10, 3rd paragraph With regard to claim 11 and similar claims, see Fig. 8 and description thereof. With regard to claim 11 and similar claims, see at least page 10, second paragraph. With regard to claim 12 and similar claims, see at least page 10, 3rd paragraph. With regard to claims 13-15 and similar claims, it is clear from Holman that first data bus (323) is capable of transmitting all known signal types. With regard to claims 46-61, one practicing the device of Holman et al. would have performed the same steps set forth in method claims 46-61.

U.S. Patent Nos. 6,014,037 to Gabara et al., 6,356,984 to Day et al., and 6,233,253 to Settle et al. are cited as relevant art.

Any inquiry concerning this communication should be directed to Khanh Dang at telephone number 703-308-0211.

Khank Pany

Khanh Dang Primary Examiner